

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of:

Noriaki HIRAGA

Application No.: 09/625,643

Filed: July 25, 2000

For: SEMICONDUCTOR INTEGRATED  
CIRCUIT DEVICE WITH  
ENHANCED RESISTANCE TO  
ELECTROSTATIC BREAKDOWN



Group Art Unit: Unassigned

Examiner: Unassigned

Commissioner for Patents  
Washington, D.C. 20231

Sir:

RECEIVED  
OCT 31 2000  
TECHNOLOGY CENTER 2800

**PRELIMINARY AMENDMENT**

Prior to initial examination, please amend the present application as follows:

**IN THE DRAWINGS:**

Please substitute Figs. 2A, 2B, 4A, 4B, 5A, 5B and 6 with the corresponding figures of the substitute drawing sheets in the attached Request for Approval of Drawing Changes.

**IN THE ABSTRACT:**

Please amend the abstract as follows:

spanning lines 10-11, delete "the other" and insert --another-- in its place.

**IN THE SPECIFICATION:**